

November 2013

ISL9K3060G3

60 A, 600 V, STEALTH™ Dual Diode

Features

- Stealth Recovery t_{rr} = 36 ns (@ I_F = 30 A)
- Max Forward Voltage, V_F = 2.4 V (@ T_C = 25°C)
- 600 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- RoHS Compliant

Applications

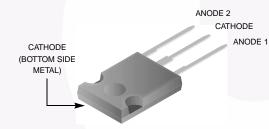
- · Switch Mode Power Supplies
- · Hard Switched PFC Boost Diode
- UPS Free Wheeling Diode
- Motor Drive FWD
- SMPS FWD
- Snubber Diode

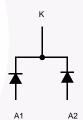
Description

The ISL9K3060G3 is a STEALTHTM dual diode optimized for low loss performance in high frequency hard switched applications. The STEALTHTM family exhibits low reverse recovery current (I_{RR}) and exceptionally soft recovery under typical operating conditions. This device is intended for use as a free wheeling or boost diode in power supplies and other power switching applications. The low I_{RR} and short ta phase reduce loss in switching transistors. The soft recovery minimizes ringing, expanding the range of conditions under which the diode may be operated without the use of additional snubber circuitry. Consider using the STEALTHTM diode with an SMPS IGBT to provide the most efficient and highest power density design at lower cost.

Package

JEDEC STYLE TO-247





Symbol

Device Maximum Ratings (per leg) T_C = 25°C unless otherwise noted

Symbol	Parameter	Rating	Unit	
V_{RRM}	Repetitive Peak Reverse Voltage	600	V	
V _{RWM}	Working Peak Reverse Voltage	600	V	
V _R	DC Blocking Voltage	600	V	
I _{F(AV)}	Average Rectified Forward Current (T _C = 125°C) Total Device Current (Both Legs)	30 60	A A	
I _{FRM}	Repetitive Peak Surge Current (20kHz Square Wave)	70	Α	
I _{FSM}	Nonrepetitive Peak Surge Current (Halfwave 1 Phase 60Hz)	325	Α	
P _D	Power Dissipation	200	W	
E _{AVL}	Avalanche Energy (1A, 40mH)	20	mJ	
T _J , T _{STG}	Operating and Storage Temperature Range	-55 to 175	°C	
T _L T _{PKG}	Maximum Temperature for Soldering Leads at 0.063in (1.6mm) from Case for 10s	300 260	°C	
	Package Body for 10s, See Techbrief TB334			

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

Device Marking K3060G3		Device	Package Packing Methode		Tape W	'idth	Quantity 30	
		ISL9K3060G3	TO-247-3L	Tube	N/A			
Electric	al Char	acteristics (per le	g) T _C = 25°C ເ	unless otherwise noted	ł			
Symbol		Parameter	Te	st Conditions	Min	Тур	Max	Units
Off State	Characte	eristics						
I _R	Instantaneo	ous Reverse Current	V _R = 600 V	T _C = 25°C	-	-	100	μΑ
			T _C = 125°C	-	-	1.0	mA	
n State	Characte	eristics						
V _F	Instantaneous Fo	ous Forward Voltage	ge I _F = 30 A	T _C = 25°C	-	2.1	2.4	V
'				T _C = 125°C	-	1.7	2.1	V
· · · · · · · · · · · · · · · · · · ·	Characte	oriotico		1 0	1			
•	Characte Junction Ca		V _R = 10 V, I _F	- O A	Ι -	120	-	nE.
СЈ	Junction Ca	араспапсе	v _R = 10 v, I _F	= U A	_	120	_	pF
witchin	g Charac	teristics						
t _{rr} Reverse I		ecovery Time	$I_F = 1A$, $dI/dt = 100 A/\mu s$, $V_R = 30 V$		-	27	35	ns
			$I_F = 30 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	36	45	ns
t _{rr}	Reverse Re	ecovery Time	$I_F = 30 \text{ A},$ $dI_F/dt = 200 \text{ A/}\mu\text{s},$ $V_R = 390 \text{ V}, T_C = 25^{\circ}\text{C}$		-	36	-	ns
I _{rr}	Reverse Re	ecovery Current			-	2.9	-	Α
Q _{rr}	Reverse Re	ecovered Charge	$V_{R} = 390 \text{ V},$	-	55	-	nC	
t _{rr}	Reverse Re	ecovery Time	$I_F = 30 \text{ A},$ $dI_F/dt = 200 \text{ A/}\mu\text{s},$		-	110	-	ns
S	Softness Fa	actor (t _b /t _a)			-	1.9	-	
I _{rr}	Reverse Re	ecovery Current		$V_R = 390 \text{ V},$		6	-	Α
Q _{rr}	Reverse Re	ecovered Charge	T _C = 125°C			450	-	nC
t _{rr}	Reverse Re	ecovery Time	I _F = 30 A,		-	60	-	ns
S	Softness Fa	actor (t _b /t _a)	dI _F /dt = 1000 A/μs, V _R = 390 V, T _C = 125°C			1.25	-	
I _{rr}	Reverse Re	ecovery Current				21	-	Α
Q _{rr}	Reverse Re	ecovered Charge				730	-	nC
dI _M /dt	Maximum o	di/dt during t _b				800	-	A/µs
hermal	Characte	ristics						
$R_{\theta JC}$	Thermal Re	esistance Junction to Case			-	-	1.0	°C/W
$R_{\theta JA}$	Thermal Re	esistance Junction to Ambier	nbient TO-247		-	-	30	°C/W

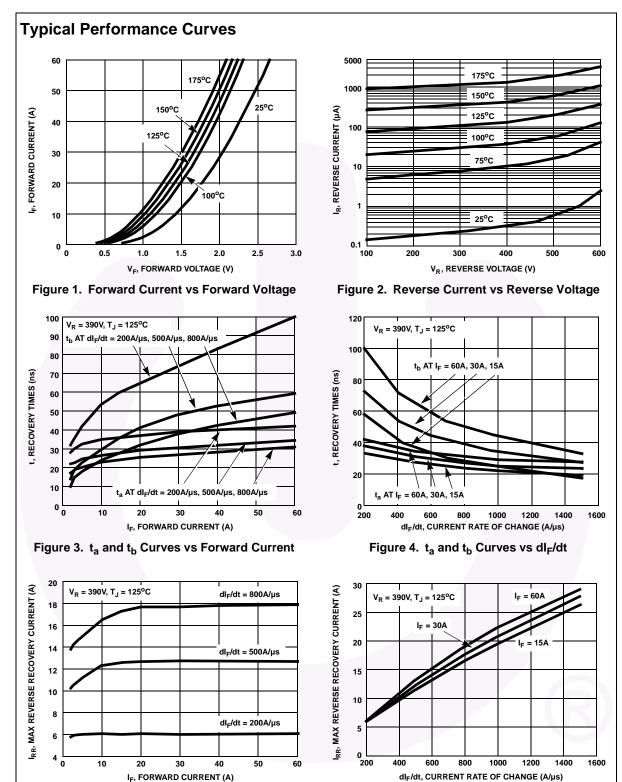


Figure 5. Maximum Reverse Recovery Current vs

Forward Current

Figure 6. Maximum Reverse Recovery Current vs

dl_F/dt

V_R = 390V, T_J = 125°C REVERSE RECOVERY SOFTNESS FACTOR I_F = 60A I_F = 30A 1.5 0.5 200 1400 600 800 1000 1200 dI_F/dt, CURRENT RATE OF CHANGE (A/μs)

Typical Performance Curves (Continued)

REVERSE RECOVERED CHARGE (nC) 600 $I_F = 15A$ 200 200 1000 dl_F/dt, CURRENT RATE OF CHANGE (A/µs)

1000

800

Figure 7. Reverse Recovery Softness Factor vs dl_F/dt

Figure 8. Reverse Recovered Charge vs dl_E/dt

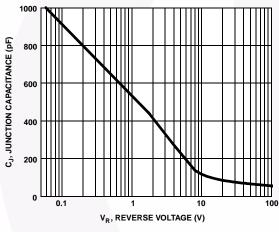
I_F = 60A

I_F = 30A

1400

1600

 $V_R = 390V, T_J = 125^{\circ}C$



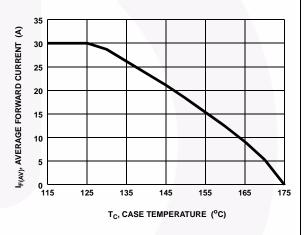


Figure 9. Junction Capacitance vs Reverse Voltage

Figure 10. DC Current Derating Curve

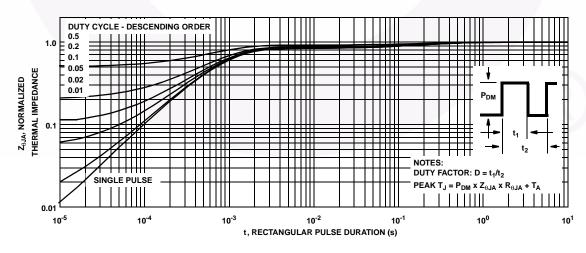
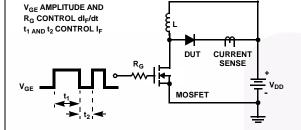


Figure 11. Normalized Maximum Transient Thermal Impedance

Test Circuit and Waveforms



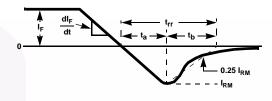
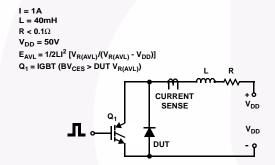


Figure 12. t_{rr} Test Circuit

Figure 13. t_{rr} Waveforms and Definitions



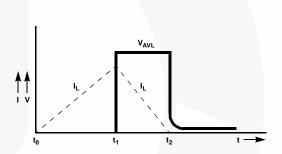
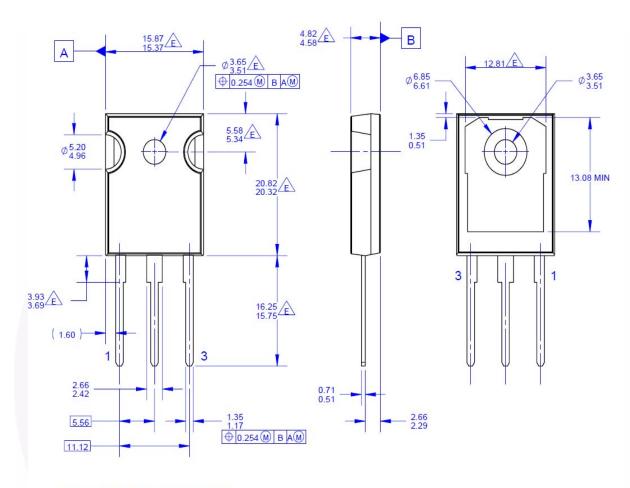


Figure 14. Avalanche Energy Test Circuit

Figure 15. Avalanche Current and Voltage Waveforms

TO247-3L



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. PACKAGE REFERENCE: JEDEC TO-247, ISSUE E, VARIATION AB, DATED JUNE, 2004.
- DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- ALL DIMENSIONS ARE IN MILLIMETERS.
 DRAWING CONFORMS TO ASME Y14.5 1994

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Figure 16. TO-247, Molded, 3LD, Jedec Option AB

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